• • • • • • • • • • • • • • • • • • • •				2133	G-P-12784
AMENDMENT TRANSMITTAL LETTER (Large Entity)  Applicant(s): Shuichi Takayama et al.  Docket No.  NAK1-BG86					
		ng Date JUN 1 2001 28, 1999	Examiner Kakali Chaki		Group Art Unit 2784
Invention: PROCESSOR FOR EXECUTING INSTRUCTIONS IN UNITS THAT ARE UNRELATD TO THE UNITS IN WHICH INSTRUCTIONS ARE READ, AND A COMPILER, AN OPTIMIZATION APPARATUS, AN ASSEMBLER, A LINKER, A DEBUGGER, AND A DISASSEMBLER FOR SUCH PROCESSOR					
TO THE ASSISTANT COMMISSIONER FOR PATENTS:  Transmitted herewith is an amendment in the above-identified application.  The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA	RATE	ADDITIONAL
TOTAL CLAIMS	48 -	48 =	0	x \$18.0	<del></del>
INDEP. CLAIMS	· 11 -	11 =	0	x \$80.0	00 \$0.00
Multipl Depende	nt Claims (check if app	olicable)		<u> </u>	\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT \$0.00					
No additional fee is required for amendment.  Please charge Deposit Account No. in the amount of A duplicate copy of this sheet is enclosed.  A check in the amount of to cover the filing fee is enclosed.  Technology Center 2100  A check in the amount of to cover the filing fee is enclosed.  The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 16-2462  A duplicate copy of this sheet is enclosed.  Any additional filing fees required under 37 C.F.R. 1.16.  Any patent application processing fees under 37 CFR 1.17.  Dated: June 8, 2001  I certify that this document and fee is being deposited on 06/08/01 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C.					
cc:				Danie	Mailing Correspondence el Kerby Person Mailing Correspondence

NAK1-BG86



## PATENT APPLICATION

ref on 6/20/01

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Shuichi Takayama, et al.

Serial No.: 09/301,284

Filed: April 28, 1999

For: PROCESSOR FOR EXECUTING

INSTRUCTIONS IN UNITS THAT ARE

UNRELATED TO THE UNITS IN WHICH INSTRUCTIONS ARE READ,

AND A COMPILER, AN

OPTIMIZATION APPARATUS, AN

ASSEMBLER, A LINKER, A

DEBUGGER AND A DISASSEMBLER

FOR SUCH A PROCESSOR

Examiner: T. Vo

Group Art Unit: 2122

June 8, 2001

Irvine, California 92614

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Technology Center 2100

## RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

In response to the Restriction Requirement of May 21, 2001, applicants request that the following be considered.